

Notice of Allowability	Application No.	Applicant(s)	
	10/600,247	SRINIVAS ET AL.	
	Examiner	Art Unit	
	Tanh Q. Nguyen	2182	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to communications filed February 26, 2007.
2. The allowed claim(s) is/are 1,2,4,6-10 and 17-20.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

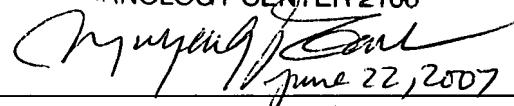
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date _____
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

TANH Q. NGUYEN
 PRIMARY EXAMINER
 TECHNOLOGY CENTER 2100


 June 22, 2007

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with the attorney of record, Mirut Dalal (Reg. No. 44,052) on June 21, 2007 to correct minor informalities, clarify the scope of the claims, remove limitations not supported by the specification, and to put the application in condition for allowance.

The application has been amended as follows:

Claim 1: replaced "logic" in line 11 with --a logic--

Claim 1: replaced "memory controller" in line 24 with --requesting node--

Claim 2: replaced "logic" in line 6 with --a logic--

Claim 6: inserted --a-- before "numerically" in line 3

7. (Currently Amended) The circuit of claim 2, wherein the logic further comprises:
a numerically controlled delay logic for receiving the read command, and for
transmitting a first signal after a first predetermined period of time after receiving the read
command and transmitting a second signal after a second predetermined period of time
after receiving the read command; and
an OR gate for transmitting the signal indicating the presence of a read transaction,

wherein the OR gate further comprises:

a first input for receiving the first signal; and

a second input for receiving the second signal; and

~~wherein the AND gate receives the signal from the memory controller directly and the signal from the memory module directly and transmits the signal to the memory controller directly.~~

Canceled claims 11-16.

17. (Currently Amended) A circuit for transferring data from memory, said circuit comprising:

a memory controller, wherein the memory controller is operable to transmit a read request;

a memory module, wherein the memory module is operable to transmit data and a signal indicating transmission of the data from the memory module;

a logic connected to the memory controller, wherein the logic is operable to transmit another signal to the memory controller indicating the transmission of the data from the memory module, wherein the logic further comprises:

an AND gate connected to the memory controller, said AND gate operable to transmit the another signal to the memory controller after a time interval, wherein during the time interval, the AND gate receives the a signal from the memory controller and the signal indicating transmission of the data from the memory module; and

a printed circuit board connected to the memory controller, the memory module,

and the logic, wherein the printed circuit board is connected to transmit the read request to the memory module, and the printed circuit board is connected to transmit the signal indicating transmission of the data from the memory module to the logic.

18. (Currently Amended) The circuit of claim 17, wherein the logic further comprises:

a first numerically controlled delay logic;
a second numerically controlled delay logic connected to the first numerically controlled delay logic; and
an OR gate connected to the first numerically controlled delay logic and the second numerically controlled delay logic, and the OR gate is connected to the AND gate.

Claim 20: replaced “first printed circuit” in lines 3-4 with --printed circuit board--

Claim 20: replaced “data” in line 6 with --data from the memory module--

Cancel claim 21.

Oath/Declaration

2. The oath or declaration is defective. A new oath or declaration in compliance with 37 CFR 1.67(a) identifying this application, by application number and filing date, is required. See MPEP §§ 602.01 and 602.02. The oath or declaration is defective because it does not identify the mailing address of each inventor. A mailing address is an address at which an inventor customarily receives his or her mail and may be either a home or business address. The mailing address should include the ZIP Code

designation. The mailing address may be provided in an application data sheet or a supplemental oath or declaration. See 37 CFR 1.63(c) and 37 CFR 1.76.

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tanh Q. Nguyen whose telephone number is 571-272-4154. The examiner can normally be reached on M-F 9:30AM-7:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim Huynh can be reached on 571-272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

TANH Q. NGUYEN
PRIMARY EXAMINER
TECHNOLOGY CENTER 2100

TQN
June 22, 2007

Tanh Q. Nguyen
June 22, 2007